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A PROTOTYPE 2-D GM SD BASED IM AG ING DETECTOR FOR X-RAYS

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Abstract

The developm ent and testing of a 2-D prototype detector based on a gas m icrostrip detector (GM SD) is reported. The second spatial co-ordinate is obtained by utilising a plane of orthogonal wires as pick up electrodes. The detector is operated with the wire plane at such a potential so as not to induce any gain around the wires. This means that the high tolerances normally associated with wire planes in Multi-Wire Proportional Counters are not necessary, making the manufacture and repair of such a device relatively easier. The detector comprises of 48 individually instrumented channels in both X (GMSD strips) and Y (transverse wire plane). A specially designed encoding module has been constructed which feeds digital addresses for each event to the ISIS data taking electronics system (DAE). An intrinsic detector resolution of ~0.5mm FW HM has been measured for both dimensions which is degraded slightly by the digital resolution for the overall system . This readout method is shown to be very tolerant of a poor signal to noise ratio in the readout channels (unlike traditional analogue w ire cham ber readout system s) and perm its the operation of the GM SD at moderate avalanche gains (~1000). This helps to maxim is the rate and lifetim e perform ance of the detector as well as permitting data capture rates in the MHznange.

1.Introduction

D etectors for ionising radiation based on gas counters com e in m any form s and play an important role in both photon and particle detection.

Key science areas in materials development within the UK demand large x-ray and neutron sensitive detectors with two-dimensional (2-D) readout to operate on D iam ond and on ISIS target station 2. These include: a 2-D detector for D iam ond beam line I22 to perform dynamic, non-crystalline diffraction on biological and m and ade polymer samples; a 2-D detector for SRS beam line 6.2 to complement the existing 1-D SAXS/WAXS systems, and on ISIS both the single crystal (SX) and reflectometry instruments need 200mm x 200mm 2-D detectors. This report deals specifically with the x-ray imaging applications. The extension of the technique to neutron applications is described elsewhere [1].

Imaging gas counters based on Multi-W ire Proportional Counter (MWPC) technology have a long history of successful application to x-ray diffraction [2-8]. Their properties of low noise, high detective quantum efficiency and the large dynam ic range of counting detectors have continued to make gas detectors attractive for application on Synchrotron Radiation (SR) beam lines.

We report the successful testing of a small prototype two dimensional gas m icrostrip detector of dimensions ~50×50mm capable of MHz readout rates. Several other institutions have made 2-D imaging devices based on GMSD sutilising various m ethods for obtaining the second dimension. One approach is to use back pads on the GMSD structure [9-11]. A nother approach is to modify the GMSD structure [12]. We have follow ed the approach of Budtz-Jorgensen [13] in using wire plane close to the plate surface to pick up the second dimension.

2.The 2-D GM SD Detector

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The GM SD is intrinsically a 1-D device and lends itself to very fast operation with channel-by-channel readout using high density, low noise electronics, as has been demonstrated in x-ray applications such as ref [14]. However, the applications discussed above dem and full 2-D readout. This has been achieved in various ways (as referenced above), how ever in the very high rates of Synchrotron beam s, the long-standing (and successful) analogue m ethods tend to reach their lim its well below the M H z capability dem anded. The perform ance and lim itations of channel-by channel readout in the 1-D case has been explored at RAL in practical application to x-ray diffraction and neutron beam s in parallel with M onte C arlo m odelling studies [14-17]. The results of these studies can be sum m ed up as follow s:

In spite of considerable spreading (by diffusion and fast electron range) of the prim ary signal, the spatial resolution is essentially governed by the strip width (i.e. SD (sigm a) ≈ W //12, where SD is the standard deviation and W is the strip width). Because of beam /sam ple sizes this means that the spatial resolutions of ~0.5mm FW HM attainable by this technique are appropriate form any applications.

- This resolution is very insensitive to the signal to noise ratio in the readout am plifier enabling low gains (~1000 for keV x-rays) to be used in the GM SD.
- The resulting low gain/signal size optim ises the rate capability and the lifetim e of the detector important in the Synchrotron environm ent.
- The main demerit of this readout approach is that it puts a high dem and on the uniform ity of response of the readout electronics. This is typically a few % and the channel sharing effects have been shown by the M C model to am plify this differential non-linearity by factors of up to two (depending on the detailed design).
- A final draw back of the m ethod is that, because of the signal sharing between channels precise pulse height inform ation (i.e. x-ray energy) is not available. Fortunately in m any beam applications this is not a critical requirem ent.

The purpose of the prototype detector described in this report is to explore the possibility of generalising the channel-by-channel readout method to achieve full 2-D in aging capability. While the need to correlate the X (anode) signals with the Y (orthogonal wire plane) signals forces the readout to be global (and therefore more rate-limiting than the 1-D case), the occupancy time (i.e. the intrinsic length of the x-ray pulse of ~200ns) is applied to only one discriminator circuit at a time so that the correlating and coding electronics can, in principle, perform considerably faster (at M H z rates).

In order to achieve pick-up of the GMSD induction pulse in the direction orthogonal to the anodes (Y) a wire plane of 1mm pitch was set close to the active plate surface at a distance of less than one anode pitch. This method was chosen for several reasons:

- The poorm echanical properties of the sem i-insulating S8900 glass (essential for stable, long term operation) precluded using patterned Y electrodes on the rear face since the plate thickness would have to be ≈0.3mm and experience has shown that this is too thin for adequate m echanical strength.
- The low wire capacitance (compared with electrodes on the glass) considerably eases the dem ands on the Y pream plifiers.
- The wires are robust (0.1mm diam eter) and held at the equilibrium potential of the site above the plate so that the dem ands of tolerance in positioning and tension are greatly reduced from those of the typical Multi-W ire Proportional Counter.

2.2 D etailed design

Figure 1 shows a schematic cross-section of the prototype 2-D detector. The S8900 glass plate is glued to a printed circuit board, and the wire plane for the second co-ordinate is mounted from four spacers at the glass edges some $250\mu m$ away from the plate surface. A thin ($5\mu m$) alum inium drift plane is mounted 6.1mm from the

glass surface to form the x-ray conversion volume. The detector is a legacy from a particle physics project [18] having an anode width of $10\mu m$, a cathode width of $90\mu m$ and a pitch of $300\mu m$, resulting in the pitch and the conversion volume being less than ideal for x-ray detection. The wire plane is constructed from gold plated tungsten wires of $100\mu m$ diameter, with 48 wires on a pitch of 1mm. The anodes of the glass plate are bussed together in groups of four in order to approximately match the wire plane pitch (giving a pixel size of 1.2mm by 1mm). 48 groups of anodes are used which gives us an active area of 57.6mm in X by 48mm in Y.

The GM SD is typically run with the readout anodes at earth, the plate cathodes at -560V and the drift plane held constant at -2kV. The wire plane is held close to the equipotential voltage between drift and plate, therefore ensuring that the Y wire plane does not influence the gas gain. This has the advantage that the wire plane can then be made without the high tolerances norm ally associated with wire-based detectors. The structure of figure 1 is housed in a gas tight enclosure with a thin alum inised m ylar window above the drift electrode to permit entry of the x-rays. Gas ports and electrical sockets are provided for interface to the readout system. For the present limited tests a gas filling of argon + 25% isobutane was used. For serious long term use the isobutane w ould be replaced with dim ethylether [19].

2.3 Operating Characteristics

The initial testing of the detector was conducted using the typical charge pream plifier/m ain am plifier/pulse height analyser chain. The x-rays can be used to calibrate the gas gain in the usual way and figure 2 shows the typical gas countergain curve (gain versus cathode voltage, V_c). It should be stressed that the lim it shown in the figure is not the spark lim it of the detector. In order for the detector to operate successfully, the signal size induced on the Y w ires m ust be large enough to generate an adequate signal to noise ratio in any Y readout channel. In practice only 14% of the anode signal is induced on any given Y w ire. V ariation of the w ire plane voltage (V_w) produces the useful gas gain (figure 3), but in practice the w ire plane is set close to the cathode voltage.

2.4 Electronic readout

The 48 channels in X and Y are individually instrum ented with pream plifier, post amplifiers and discrim inators as shown schematically in figure 4. The preamplifiers used are 16 channels fast charge am plifiers which have been developed elsewhere [18]. The pulse shaping am plifier was specifically designed to plug into the pream p and is described in [20]. These can be seen in the photograph in figure 5. Figure 6 shows typical pulses from both X and Y for ⁵⁵Fe x-rays.NIM based Lecroy 612 post am plifiers increase the signal size further before the signals are passed to NE 4684 N IM based discrim inators. From here the digital signal is then passed to an in-house built encoding and combining module, which is described in detail in Appendix 1. This module determ ines the validity of the events, addresses the events, com bines the data and sends it to the ISIS data taking electronics (DAE), which has been described elsewhere [21]. The module takes its master trigger from the anode signal and determ ines if it is a valid event. This is done using an EPROM, which is program med to accept either single or double (ad jacent) channel hits. The EPROM then books for events on the other 47 channels to veto out any ghost hits. The event is then addressed as a six bit word (dual hits are assigned to the least significant bit). This is repeated

for Y, and the module then combines the two readout channels by forming a coincidence between the synchronized X and Y pulses. The x-ray event address is then sent to the DAE. The data capture efficiency has been measured to be greater than 90%.

3. System perform ance

3.1 Im aging

The detector is usually used with a flowing gas mixture, but was sealed off for use close to the DAE electronics, and was re-gassed on a daily basis. Figure 7 shows the data obtained, when a mask made from copper tape with letters 18mm high and transmission slots of 3-3.5mm was placed on the front window of the chamber and illuminated with 5.9keV x-rays.

In order to measure the intrinsic resolution (unlimited by the digital readout) of the detector, a 0.5mm collimated slit beam of 5.9keV x-rays was set up and scanned across two consecutive pixels in X and two adjacent wires in Y. The X distribution, shown in figure 8, was obtained by summing in Y to produce an X histogram for each scan position. A series of such X scans were obtained. Each distribution was normalised to its centroid and all the distributions summed. This process demonstrates the intrinsic detector resolution with a statistically enhanced digital resolution. The procedure was repeated for Y and the results are shown in figure 9. Gaussian fits to these figures yield a sigm a of 0.349 channels for X and 0.383 channels for Y. The detector resolution is then calculated by deconvoluting the beam width (0.726mm at half depth) out, which gives a FW HM resolution of 0.459mm in X and 0.534mm in Y. The practical readout spatial resolution of the detector is of course convolved with the digital resolution of the readout system W //12, where W is the readout pitch, leading to FW HM values of 0.938mm and 0.866mm for X and Y respectively.

A 0.5mm collim ated slit beam was then scanned across the entire length of the detector, in both dimensions, in 1mm steps. Figure 10 shows how the measured position and sigm a, the standard deviation, vary across the detector length through the profile along Y = 25. Figure 11 shows this variation for the Y data along X = 25. The oscillatory behaviour of the sigm a in X is due to M oire interference between the 1mm positions of the collim ated beam and the 1.2mm pixel spacing. The large sigm a in Y at the ends of the scans are thought to be due to asymmetric event collection.

Flooding the entire area of the detector with x-rays highlights any nonuniform ities present in both the electronics and the detector. The discriminator thresholds were adjusted manually in order to minim is electronic non-linearities. Figure 12 shows two profiles, across X = 25 and Y = 25, from a flood of the counter once the adjustment has been made. The RM S error of the mean value gives a value of 3.2% in X and 6.2% in Y if the last point is ignored. This outlier is thought to be due to the detection of events outside the active area, as proved when the beam is tightly colliminated, but why this behaviour is not seen at the other end is not clear.

The rate perform ance of the system was measured by using a range of alum inium attenuating sheets to adjust the source rate. As figure 13 shows the global system will acquire data at up to one million counts per second. The global dead time (counter, encoder and DAE acquisition system) is ~200ns. No tests have been carried out to characterise the rate capability of the detector and encoder separately. The ISIS DAE electronics is limited to a rate of the order observed (a new version is being made

which will double this perform ance). The potential of the counting system is almost certainly higher. In particular, the parallel nature (48 channels) of the triggering system means that the 200ns occupancy of an x-ray pulse does not, in principle, determ ine the trigger rate of the global system, which could be of the order of a factor of five faster. This would perm it rates of greater than 10M Hz (provided the data capture system can handle this event rate).

The poor signal to noise ratio in the Y channels tends to lead to som e degree of corruption and loss of events and so reduce the encoder efficiency below 100%, as wellas degrading the uniform ity of response in this dimension.

4.D iscussion

The results presented above from the prototype 2-D GM SD show that a high rate (>M Hz) 2-D x-ray detector design with sub-millimetre spatial resolution can be based on the principles described. The target design aims at an active area of 200mm x 200mm with a plate anode pitch of 0.5mm and a Y wire plane of the same pitch. In this case the digital contribution to the resolution (0.34mm FW HM) would scarcely increase the intrinsic resolution as measured in figures 8 and 9 ($\approx 0.5mm$ FW HM). Further work is required to define the upper rate limit of the encoding system and the connection to an adequately speedy data capture system.

It is anticipated that useful improvements in the signal to noise ratio in the Y channels and improved alignment methods for the channel sensitivities can be achieved so aiming at a differential linearity in the region of 2%.

Funding has been secured, via the centre for instrum entation, to build a 200 by 200mm version of this detector. This detector will be specifically designed to explore the use of the existing RAPID readout system at the SRS at Daresbury [8]. Initial testing will be carried out using x-rays with the gas filling at atm ospheric pressure, but a sim ilar detectorm ounted in an atm osphere of pressurised ³H e w ould be suitable for neutron in aging.

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Figure 2:Gain variation of the GM SD as a function of cathode voltage



Figure 3: Gain variation of the GM SD as a function of wire-plane voltage



Figure 4: Schem atic diagram of the electronic readout. W here A 1 is the 16 channel pream plifier; A 2 is the shaping am plifier; A 3 is the N IM postam plifier; D isc is the N IM discrim inator and PE is the prim ary encoder



Figure 5: Photograph of the fully instrum ented detector



Figure 6:Typical pulses from the GM SD pream p (left) and w ireplane pream p/shaper (right) for 55 Fe x-rays for single channels.



Figure 7: Im age of copper `ISIS ' m ask taken w ith the 2-D detector and $^{55}{\rm Fe}\,x$ -rays



Figure 8: Intrinsic detector spatial resolution using interpolated readouts for X



Figure 9: Intrinsic detector spatial resolution using interpolated readouts for Y



Figure 10:Variation of spatial resolution (sigm a) and m easured beam position as source m oved along full length of detector



Figure 11: Variation of spatial resolution (sigm a) and m easured beam position as source m oved along full height of detector



Figure 12: Uniform ity of response along lines x=25 and y=25 for flood of entire detector area



Figure 13: Rate perform ance of the detector with global flood.

<u>Appendix 1:Description of the combining and encoding module serial number</u> <u>DEG 510</u>

A block diagram of this unit is shown in the figure below. The eight inputs from the discriminator are fed into NIM to TTL converters. The converted signal is then fed into a Latch chip and an OR Gate. The O/P from the OR gate is then fed into another OR gate along with O/Ps from the other seven OR gates (part of circuit enclosed by box is repeated seven times). A signal out of the second OR gate is generated if any one or m ore of the 64 I/P s (8×8) are present. This signal from the OR gate starts off m onostable 1. The width of this pulse determ ines the total width of the latch signal and the delay after the latch. An O /P from monostable 1 triggers monostable 2, the width of which is the amount of time that the data is held by the latch for the rest of the circuitry to work. This is effectively the coincidence time window and is set at 200ns. The longer this pulse the longer the data is on the output for. 0 /P from m onostable 1 also triggers m onostable 3, the 0 /P of which is used to form the data ready pulse. The data that is held by the latch is fed into an EPROM (type AM 26C 512). This gives out a six bit address depending on which I/P was present. The EPROM can be programmed to accept various hit patterns. For the results presented above the EPROM has been program med to accept double hits as well as single channel hits. For the double hits, that is events which fire two adjacent channels, the hit is arbitrarily assigned to the address with the least significant bit.



A comparison was made between the EPROM S program med to accept just single hits and the EPROM S program med to accept single and double hits. A s expected, with the EPROM S program med to accept double as well as single events, 21% more events were accepted. For the data presented in this paper the EPROM S have been program med to accept single and double hits. If there is no valid L/P com bination then the O /P is 000000. The EPROM also generates a data ready signal, which is fed along with the data into some delay lines. The EPROM also generates a chip enable signal, which along with the signals from the other seven circuits is fed into another EPROM. This EPROM switches the TRISTATE buffer O /P is either on or into tri-state. Only one of the eight Tri-states can be on at any one time. The data from the tri-state is then converted into ECL logic. The EPROM D/R signal, the monostable D/R signal and the D/R signal from the otherm odule are allANDED together and the resulting pulse is converted to ECL. At the same time, data from the Y module is also converted into ECL. For a valid data ready pulse to occur then the follow ing must be present:

a)M onostable 3 output must be present, indicating that one or more of the 64 $\rm I/P$ channels had a signal.

b) EPROM D/R must be present indicating that the combination of input pulses was valid. In the case at the moment this is set to be either a single channel or two adjacent channels.

c)D at a ready from Y m odule m ust be present, indicating that the Y m odule has had an event that also has m et a and b criteria s.

The cycle time of the circuit has been measured to be 300ns.